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Northeast Wisconsin Technical College

## 10-660-102 053714 Digital 2: Sequential

### Course Outcome Summary

#### Course Information

<b>Description</b>	10-660-102 DIGITAL 2: SEQUENTIAL ...operation and connection of latches, RS flip-flops, JK flip-flops, and D flip-flops using timing diagram analysis, and some simple applications are studied. (Corequisite: 10-660-101, Digital 1: Logic)
<b>Total Credits</b>	1
<b>Total Hours</b>	36

#### Course History

<b>Last Revision Date</b>	12/14/2017
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#### Employability Skills

1. Communicate Effectively
2. Demonstrate Community and Global Accountability
3. Demonstrate Personal Accountability
4. Solve Problems Effectively
5. Think Critically and Creatively
6. Value Individual Differences and Abilities
7. Work Cooperatively and Professionally

#### Course Competencies

1. Perform simplification using DeMorgan's theorems.
2. Design simple circuits using alternate logic.
3. Perform general design procedures to simplify complex Boolean expressions.
4. Perform general design procedures using Karnaugh maps.
5. Describe the operation of a latch.
6. Analyze a complex circuit that uses latches.
7. Describe the operation of clocked RS flip-flops.

8. Describe the operation of clocked JK flip-flops.
9. Describe the operation of clocked D flip-flops.
10. Explain how asynchronous inputs effect the operation of a flip-flop.